

IN THE DRAWINGS

Attached are formal drawings for the present application.

Attachment:        formal drawings (7 sheets)

The diagram illustrates the 1D DCT-II architecture. It starts with inputs  $tx$ ,  $ty$ ,  $subTile$ , and  $offset$ .  $tx$  and  $ty$  are subtracted to produce  $stxo$  and  $styo$ .  $stxo$  and  $styo$  are then added to  $dpdx$  and  $dpdy$  respectively. The results are multiplied by  $10.13$  extracted. The output is then added to  $pStart$  and multiplied by  $4$ . The result is then added to  $p$  and divided by  $p/2$ . The final output is a  $9.13$  fixed-point value, which is then converted to a floating-point (FP) value. The diagram also shows a multiplier tree for the final output.



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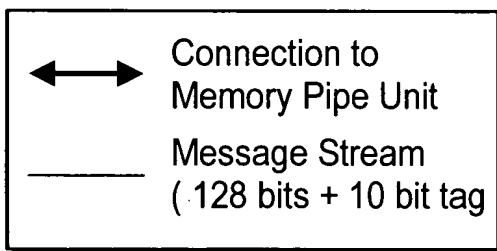
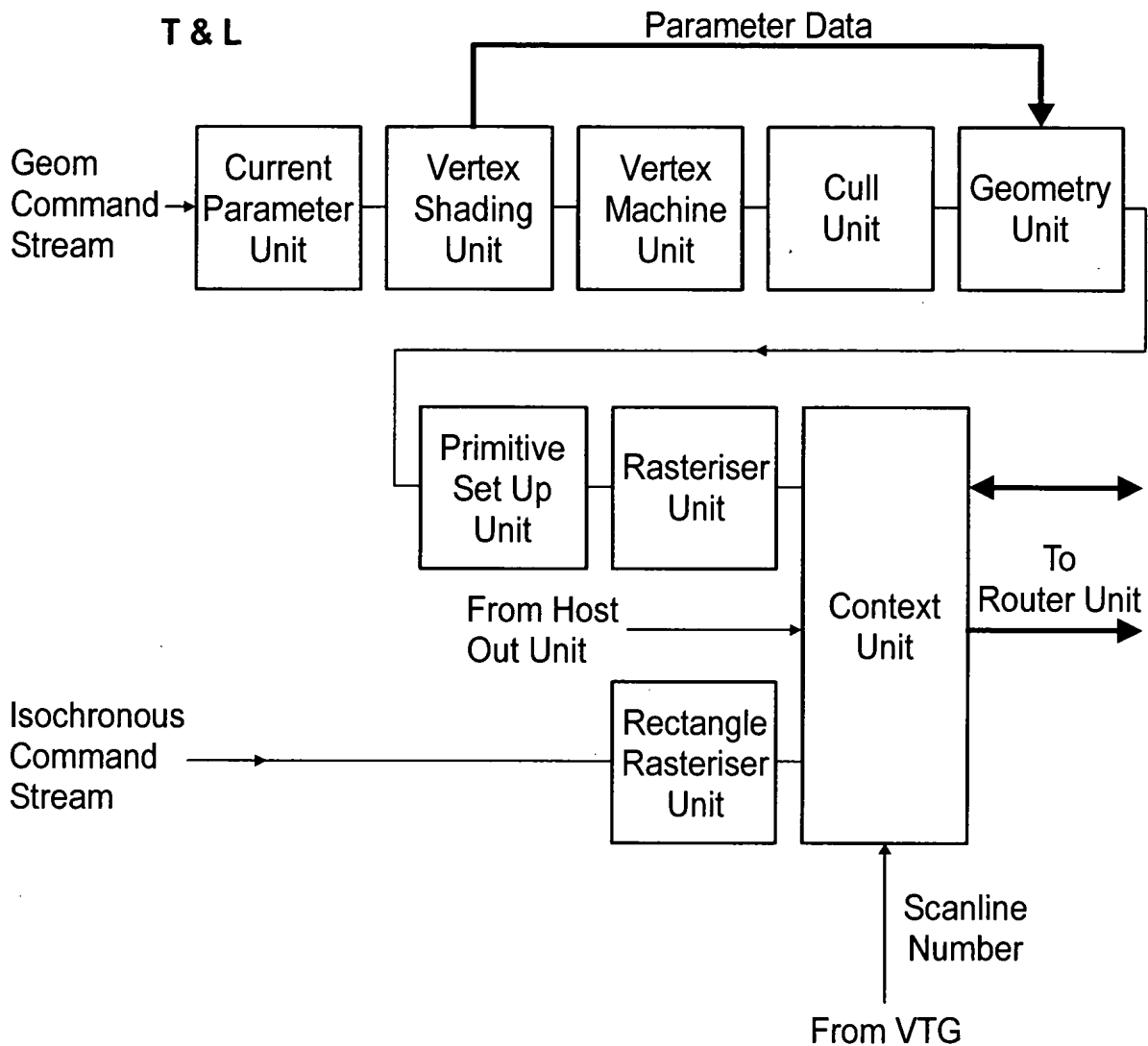
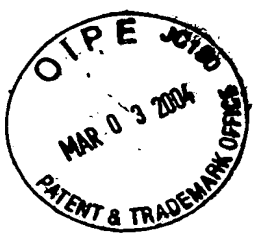


FIG. 1A



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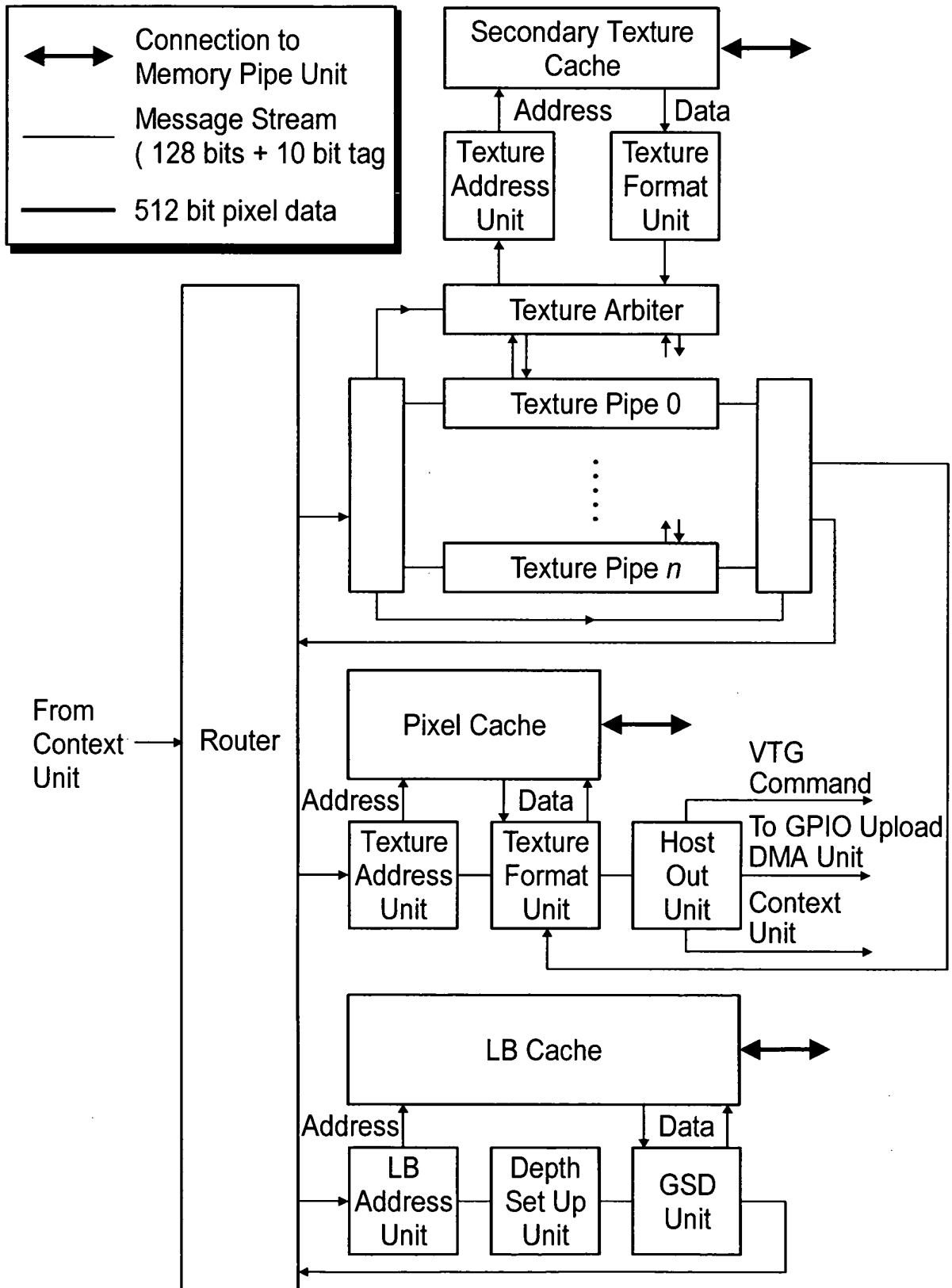


FIG. 1B



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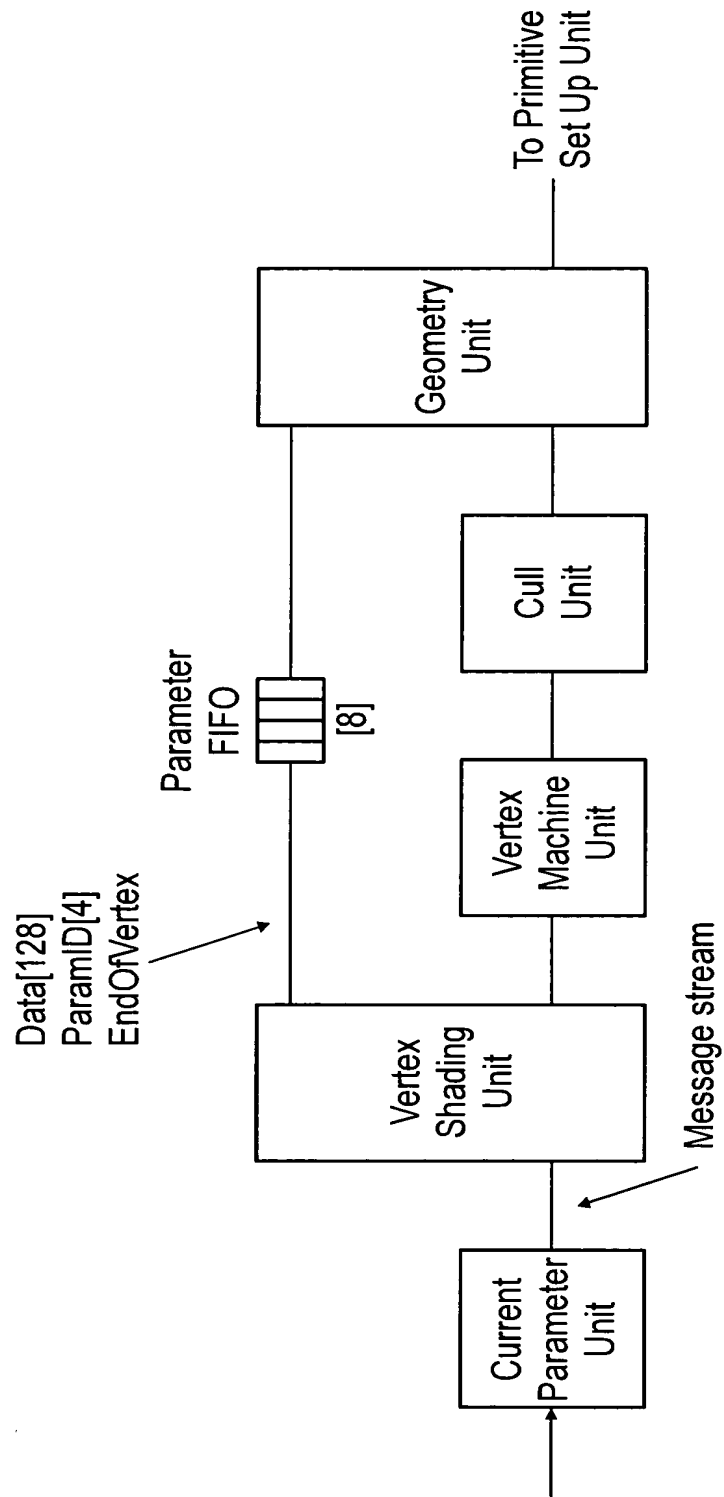


FIG. 1C



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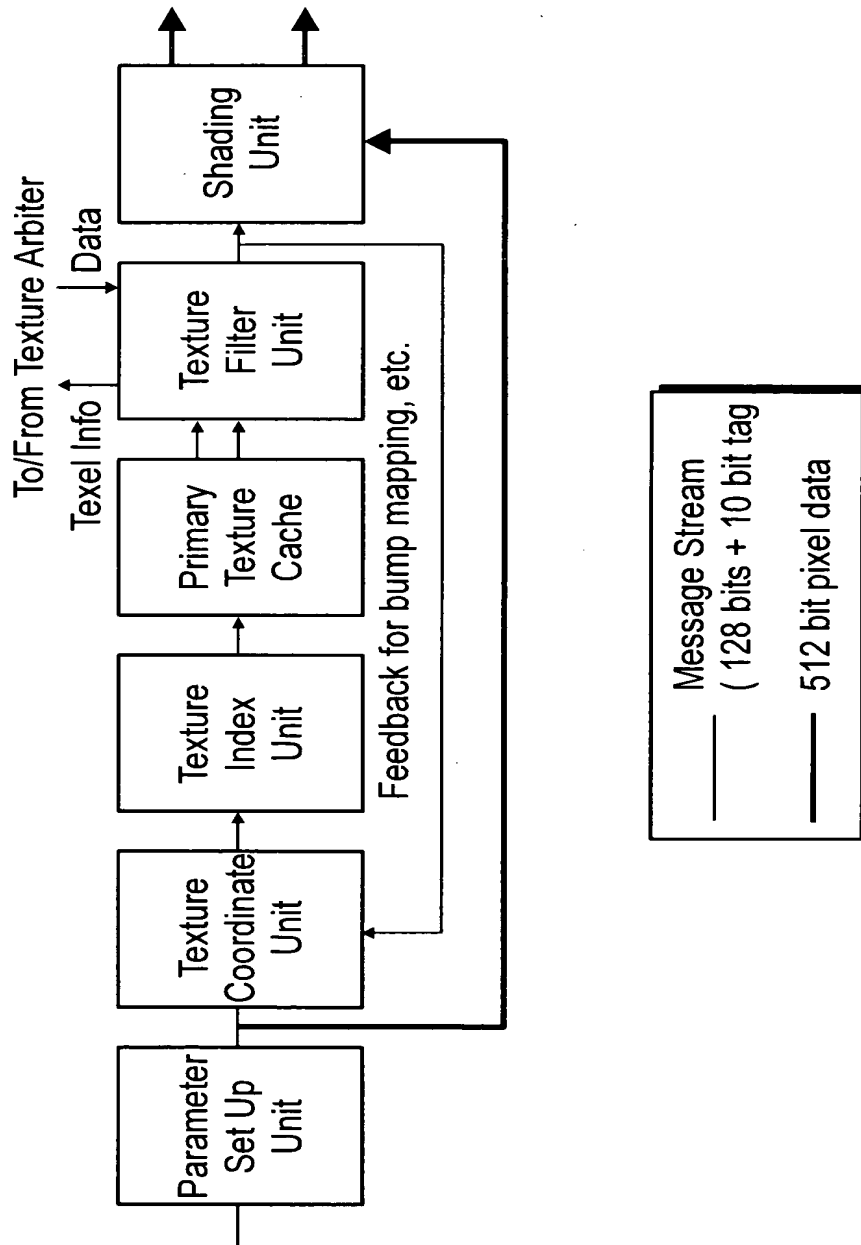


FIG. 1D



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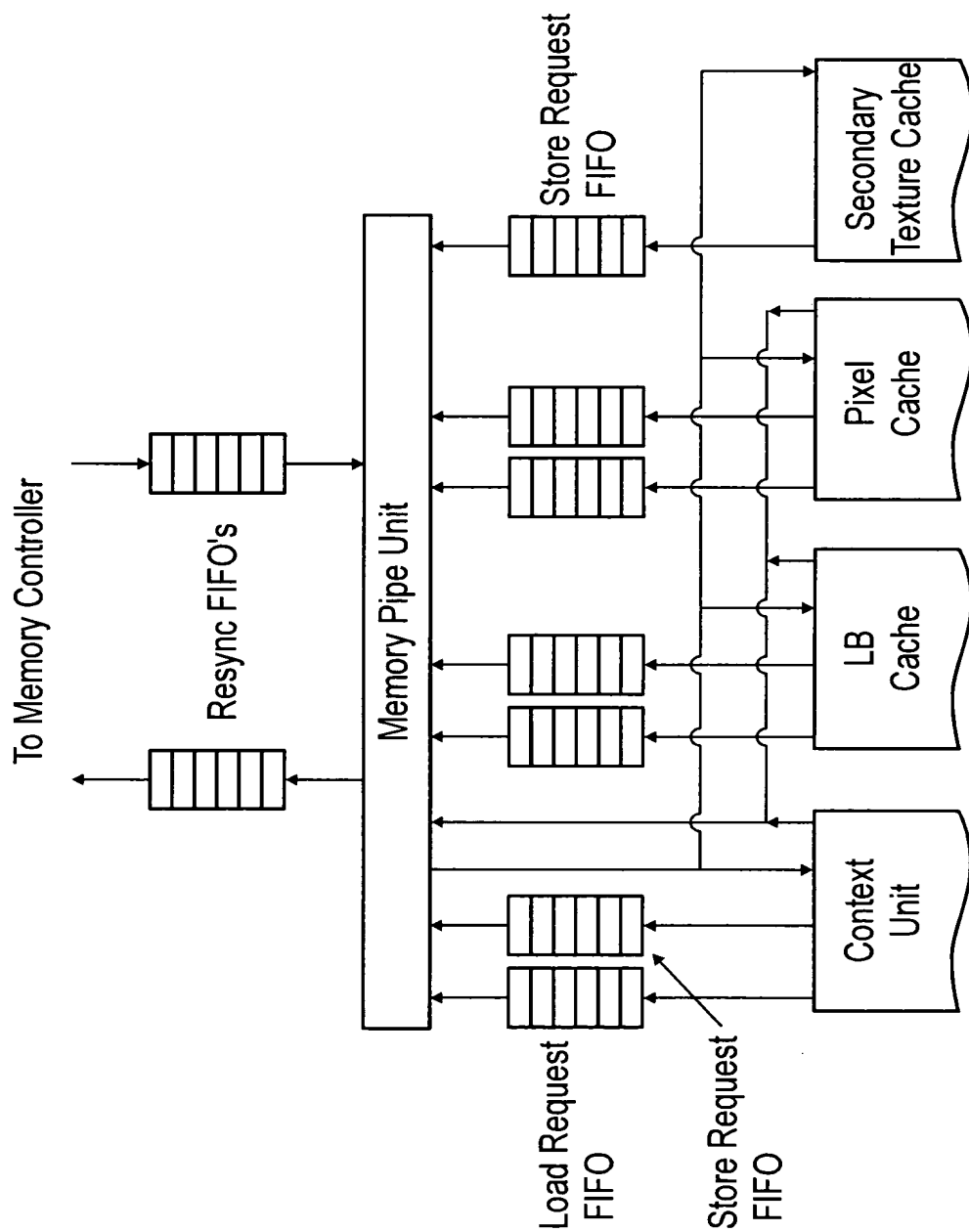


FIG. 1E



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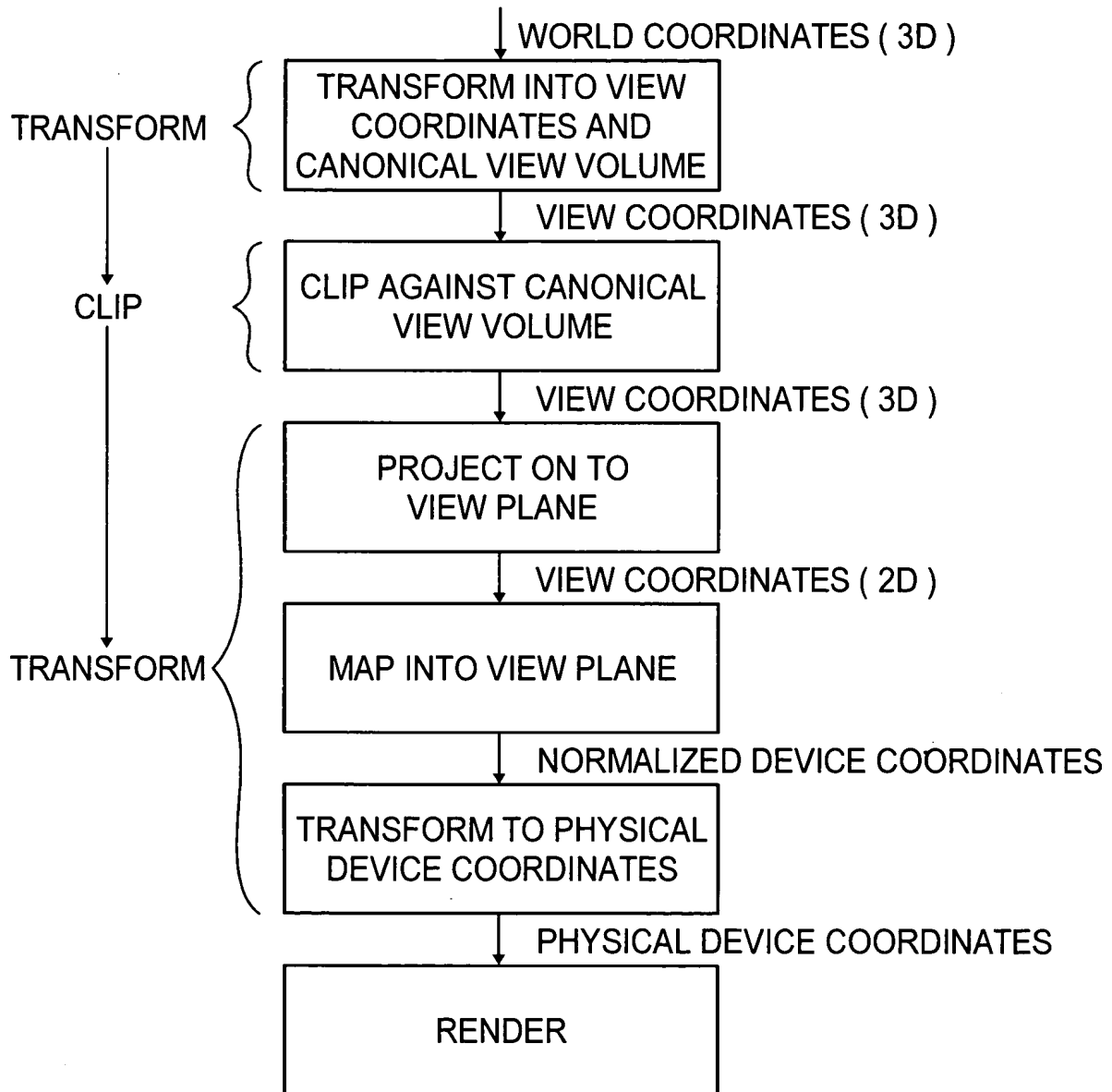


FIG. 2